

CS631-02 RISC-V Machine code 2026-03-24

Project02 due Thu 11:59 pm

Next up:

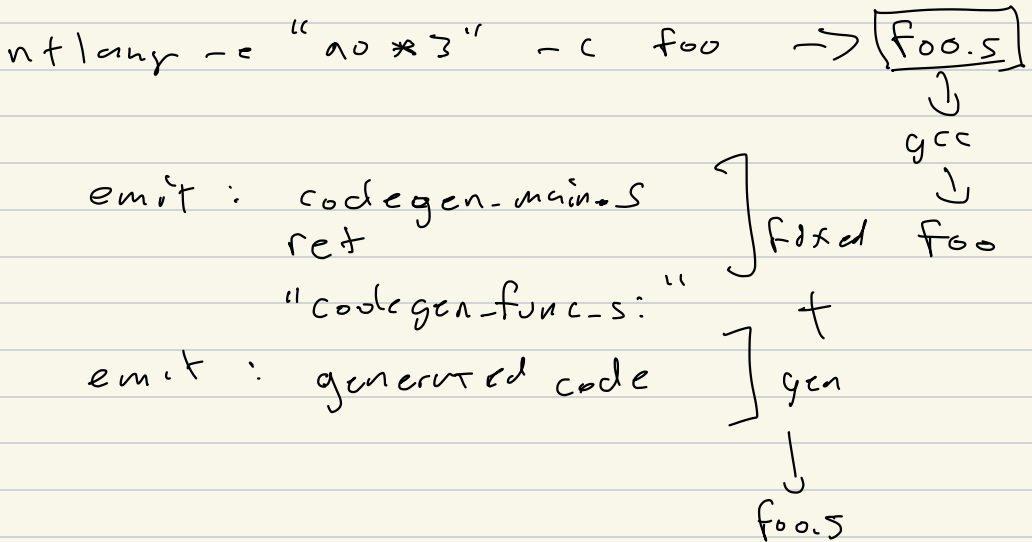
RISC-V Machine Code (ISA)

RISC-V Emulation

Cache Simulation

Project03

NTLang -c



codegen-main.s:

Project 02

Host

macOS

Windows

Linux ←

generate / run

riscv64

code

executables

Host

macOS

(arm64)

Container

Entire OS

installer.sh

Ubuntu

(arm64)

gnu

cross build

tools

rust compiler

qemu-riscv64

